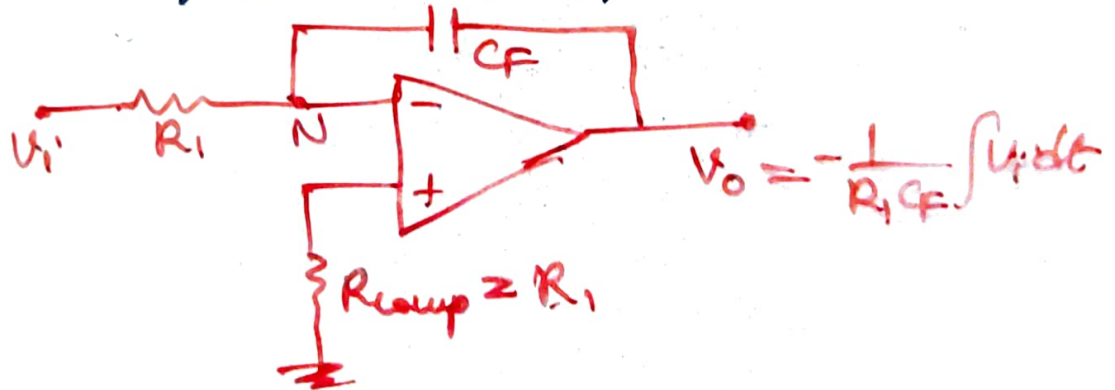


Integrator

→ Integrator provides an op voltage which is proportional to the time integral of the v_i



→ Note equation at node N is

$$\frac{v_i}{R_1} + C_f \frac{dv_o}{dt} = 0$$

$$\Rightarrow \frac{dv_o}{dt} = -\frac{1}{R_1 C_f} v_i$$

→ Integrating both sides, we get

$$\int_0^t dv_o = -\frac{1}{R_1 C_f} \int_0^t v_i dt$$

$$\Rightarrow V_o(t) = -\frac{1}{R_1 C_F} \int_0^t V_i(t) dt + V_o(0)$$

where $V_o(0)$ is the initial o/p voltage

→ In freq. domain $V_o(s) = -\frac{1}{s R_1 C_F} V_i(s)$

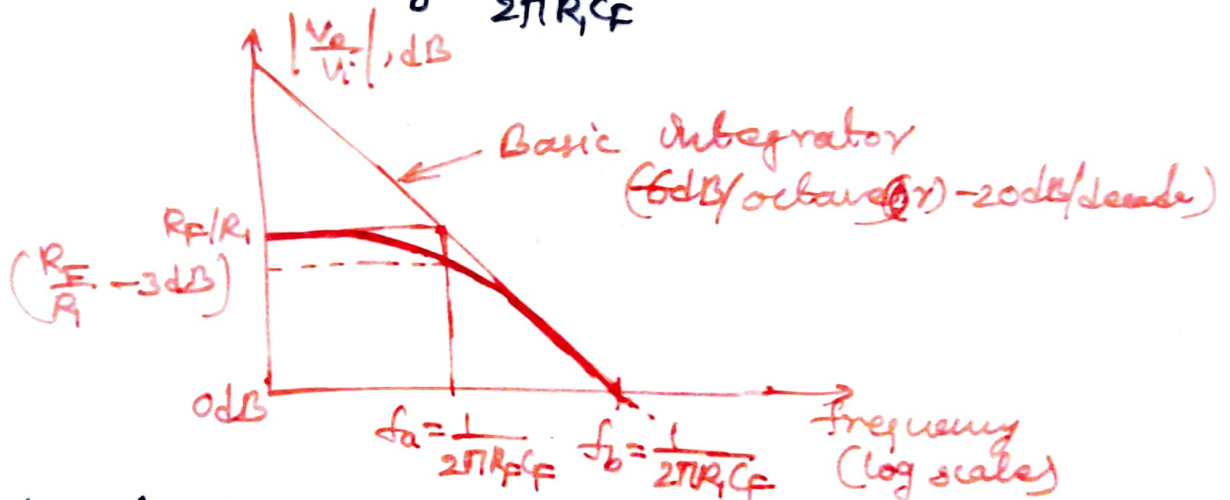
→ In steady state, $s = j\omega$ & we get

$$V_o(j\omega) = -\frac{1}{j\omega R_1 C_F} V_i(j\omega)$$

$$|A| = \left| \frac{V_o(j\omega)}{V_i(j\omega)} \right| = \left| -\frac{1}{j\omega R_1 C_F} \right| = \frac{1}{\omega R_1 C_F}$$

$$= \frac{f_b}{f}$$

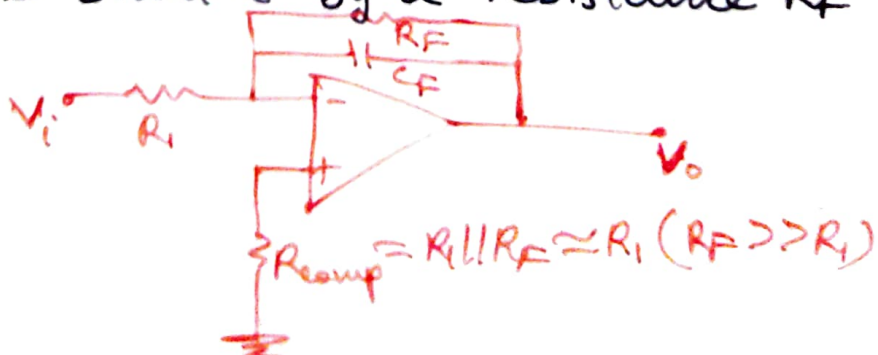
where $f_b = \frac{1}{2\pi R_1 C_F}$



→ At low freq's such as at dc ($\omega \equiv 0$), gain becomes infinite (or saturates)

Practical Integrator circuit (Lossy Integrator)

→ The gain of an integrator at low freq. (dc) can be limited to avoid saturation problem if the feedback capacitor is shunted by a resistance R_f



$$\frac{V_i(s)}{R_1} + sC_F V_o(s) + \frac{V_o(s)}{R_F} = 0$$

$$\Rightarrow V_o(s) = -\frac{1}{sR_1C_F + R_1/R_F} V_i(s)$$

→ for $s = j\omega$

$$|A| = \left| \frac{V_o}{V_i} \right| = \frac{1}{\sqrt{\omega^2 R_1^2 C_F^2 + R_1^2 / R_F^2}} = \frac{R_F / R_1}{\sqrt{1 + (\omega R_F C_F)^2}}$$

→ At low freq's gain is const. at R_F / R_1

→ The break freq. ($f = f_a$) at which gain is $0.707 (R_F / R_1)$

$$\sqrt{1 + (\omega R_F C_F)^2} = \sqrt{2}$$

→ Solving for $f = f_a$, $f_a = \frac{1}{2\pi R_F C_F}$

Q) ^{Consider} Design a practical integrator with $R_1 = 10k\Omega$, $R_f = 100k\Omega$, $C_f = 10nF$, determine the lower frequency limit of integration & study the response for i/p s (i) sine wave (ii) step i/p (iii) square wave.

Lower freq. limit of integration f_a is $f_a = \frac{1}{2\pi R_f C_f}$

$$= 159 \text{ Hz}$$

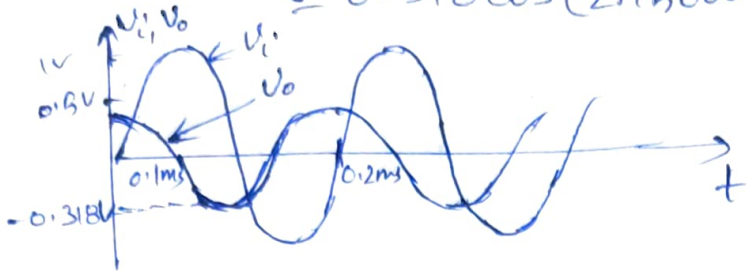
Sine wave i/p :- For an i/p of 1V peak sine wave at 5kHz the op V_o is

$$V_o(t) = -\frac{1}{R_1 C_f} \int V_c(t) dt$$

$$= -\frac{1}{10k \times 10n} \int \sin(2\pi 5000t) dt$$

$$= -10^4 \int \sin(2\pi 5000t) dt = \frac{-10^4}{2\pi 5000} [-\cos(2\pi 5000t)]$$

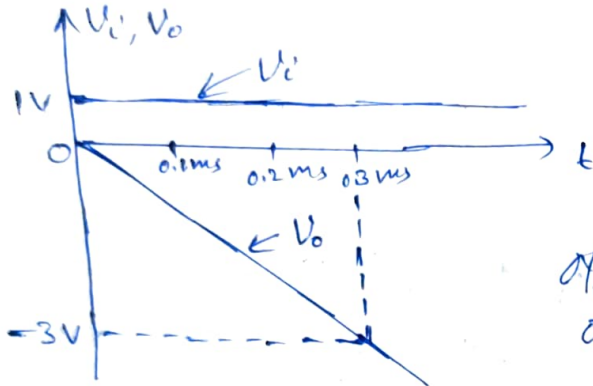
$$= 0.318 \cos(2\pi 5000t)$$



o/p is cosine wave with peak amplitude 0.318V

Step up o/p is a step voltage $V_i = 1V$ for $0 \leq t \leq 0.3ms$

the o/p voltage is $V_o = -\frac{1}{R_1 C_f} \int_0^{0.3ms} 1 \cdot dt$



$$= -\frac{1}{10k \times 10n} \left[t \right]_{t=0}^{t=0.3ms}$$

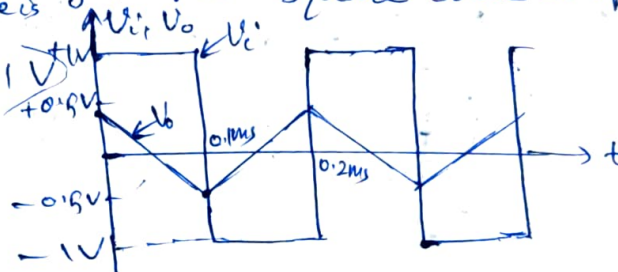
$$= -3V$$

o/p is a ramp function with slope of 10V/ms

Square wave i/p

If the o/p waveform of 5kHz 1V peak square wave is applied. The peak value of o/p for first half cycle is

$$V_o = -\frac{1}{R_1 C_f} \int_0^{0.1ms} 1 \cdot dt = -1V$$



o/p is triangular wave

Q) Design a practical integrator ckt with a dc gain of 10, to integrate a square wave of 10 kHz

$$|A|_{dc} = \frac{R_f}{R_1} \Rightarrow 10 = \frac{R_f}{R_1}$$

i/p freq. is $f = 10 \text{ kHz}$

For proper integration $f \geq 10f_a$ where f_a is break freq. of practical integrator

$$\frac{f}{f_a} \geq 10$$

$$f_a = \frac{f}{10} = \frac{10 \text{ kHz}}{10} = 1 \text{ kHz}$$

For practical integrator

$$f_a = \frac{1}{2\pi R_f C_f} \Rightarrow R_f C_f = 1.5915 \times 10^{-4}$$

$$\text{Let } R_1 = 10 \text{ k}\Omega$$

$$R_f = 10 R_1 = 10 \times 10 \text{ k} = 100 \text{ k}\Omega$$

$$C_f = \frac{1.5915 \times 10^{-4}}{100 \times 10^3} = 1.5915 \times 10^{-9} \text{ F} \\ \approx 1.6 \text{ nF}$$

$$R_{\text{comp}} = R_1 \parallel R_f = 9.09 \text{ k}\Omega$$