

→ FET Small-Signal Model

→ We can express the drain current i_D as a function of gate voltage v_{GS} & drain voltage v_{DS} by $i_D = f(v_{GS}, v_{DS})$

Transconductance g_m & Drain Resistance r_d

→ If both the gate & drain voltages are varied, the change in drain current is given by Taylor's ^{series} expansion of $i_D = f(v_{GS}, v_{DS})$

$$\Delta i_D = \left. \frac{\partial i_D}{\partial v_{GS}} \right|_{v_{DS}} \Delta v_{GS} + \left. \frac{\partial i_D}{\partial v_{DS}} \right|_{v_{GS}} \Delta v_{DS}$$

→ We can write $\Delta i_D = i_D$, $\Delta v_{GS} = v_{GS}$ & $\Delta v_{DS} = v_{DS}$, so

$$i_D = g_m v_{GS} + \frac{1}{r_d} v_{DS}$$

where $g_m \equiv \left. \frac{\partial i_D}{\partial v_{GS}} \right|_{v_{DS}} = \left. \frac{\partial i_D}{\partial v_{GS}} \right|_{v_{GS}, v_{DS}}$ is mutual

conductance or transconductance.

→ It is also designated by y_{fs} & g_{fs} & called (common source) forward transmittance

→ The parameter r_d is the drain (or o/p) resistance & is defined by

$$r_d = \frac{\partial V_{DS}}{\partial i_d} \Big|_{V_{GS}} = \frac{\Delta V_{DS}}{\Delta i_d} \Big|_{V_{GS}} = \frac{V_{DS}}{i_d} \Big|_{V_{GS}}$$

→ The reciprocal of r_d is the drain conductance g_d .

→ It is also designated by Y_{OS} & g_{OS} & called the (common-source) o/p conductance.

→
$$\mu = \frac{\partial V_{DS}}{\partial V_{GS}} \Big|_{i_d} = \frac{\Delta V_{DS}}{\Delta V_{GS}} \Big|_{i_d} = \frac{V_{DS}}{V_{GS}} \Big|_{i_d}$$

→ By setting $i_d = 0$ in $i_d = g_m V_{GS} + \frac{1}{r_d} V_{DS}$

$$g_m r_d = \frac{V_{DS}}{V_{GS}}$$

So $\mu = g_m r_d$

→ Applying the definition of g_m to $I_{DSS} = I_{DSS} \left(1 - \frac{V_{GS}}{V_P}\right)^2$

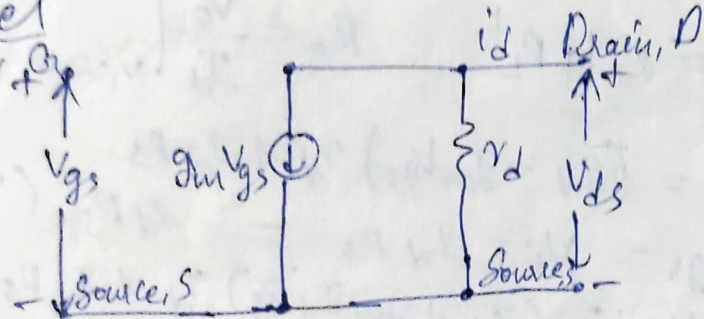
$$\begin{aligned} \frac{\partial I_{DSS}}{\partial V_{GS}} &= I_{DSS} 2 \left(1 - \frac{V_{GS}}{V_P}\right) \left(-\frac{1}{V_P}\right) \\ &= -\frac{2 I_{DSS}}{V_P} \left(1 - \frac{V_{GS}}{V_P}\right) \end{aligned}$$

So $g_m = g_{m0} \left(1 - \frac{V_{GS}}{V_P}\right)$ where g_{m0} is the value of g_m for $V_{GS} = 0$ & is given by

$$g_{m0} = \frac{-2 I_{DSS}}{V_P}$$

→ Since I_{DSS} & V_P are of opposite sign, g_{m0} is always +ve.

FET Model



→ This is low freq. small-signal FET model satisfies

$$\hat{i}_d = g_m V_{gs} + \frac{1}{r_d} V_{ds}$$